

#4
09/831525

JC08 Rec'd PCT/PTO 10 MAY 2001
PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of
Romain DESPLATS et al.
Serial No. (unknown)
Filed herewith

METHOD AND INSTALLATION FOR
FAST LOCATION OF A FAULT IN
AN INTEGRATED CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were cited by the European Patent Office in the corresponding International application Serial No. PCT/FR99/01212, filed May 21, 1999.

Respectfully submitted,

YOUNG & THOMPSON

By

Benoît Castel

Benoît Castel
Attorney for Applicants
Customer No. 000466
Registration No. 35,041
745 South 23rd Street
Arlington, VA 22202
Telephone: 703/521-2297

May 10, 2001